## CH920020037USI (WYC) (8738-668)

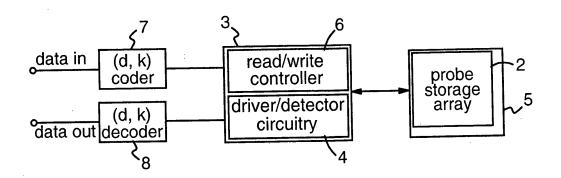
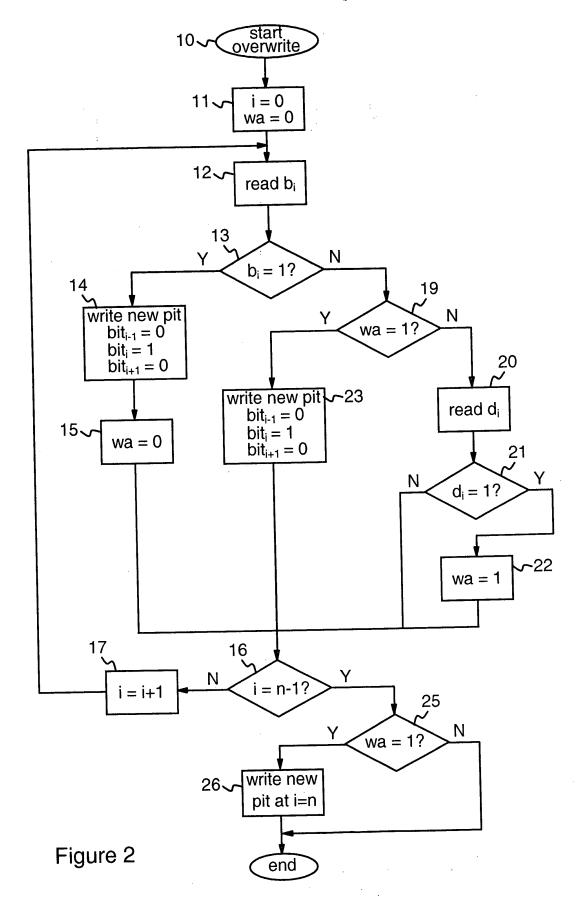


Figure 1

						<b></b>		writ (w	e a /a =		S					S	pare bit	Э
	;	<b></b>			<b></b>							į	-		•	4	<u> </u>	
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
•	1	0	1	0	0	1	0	0	0	1	0	1	0	0	1	0		
coded data b <sub>i</sub> :	0	0	0	0	1	0	1	0	0	1	0	0	1	0	0	0		
reads:	R	-	-	-	-	R	-	R	R	-	R	R	-	R	R	-		
writes:	-	W	W	W	w	-	W	-	-	W	-	-	W	-	-	W	W	
					•								•	:		:	•	
write results																		
i = 1:	0	1	0	0	0	1	0	0	0	1	0	1	0	0	1	0		
i = 2:	0	0	1	0	0	1	0	0	0	1	0	1	0	0	1	0		
i = 3:	0	0	0	1	0	1	0	0	0	1	0	1	0	0	1	0		
i = 4:	0	0	0	0	1	0	0	0	0	1	0	1	0	0	1	0		
i = 6:	0	0	0	0	1	0	1	0	0	1	0	1	0	0	1	0		
i = 9:	0	0	0	0	1	0	1	0	0	1	0	1	0	0	1	0		
i = 12:	0	0	0	0	1	0	1	0	0	1	0	0	1	0	1	0		
i = 15:	0	0	0	0	1	0	1	0	0	1	0	0	1	0	0	1		
i = 16:	0	0	0	0	1	0	1	0	0	1	0	0	1	0	. 0	0	1	

Figure 3

219 CH920020027U51(8728-468)

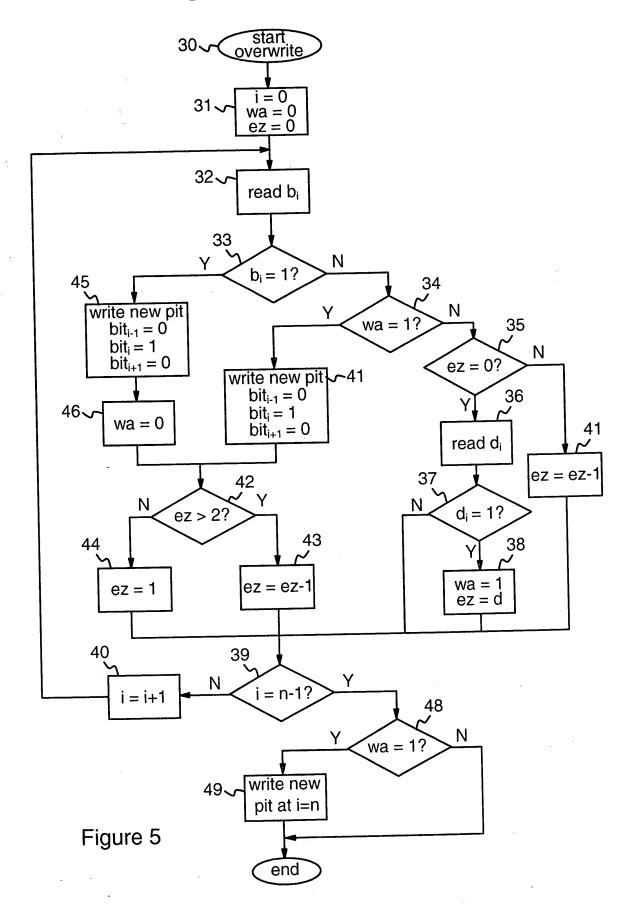


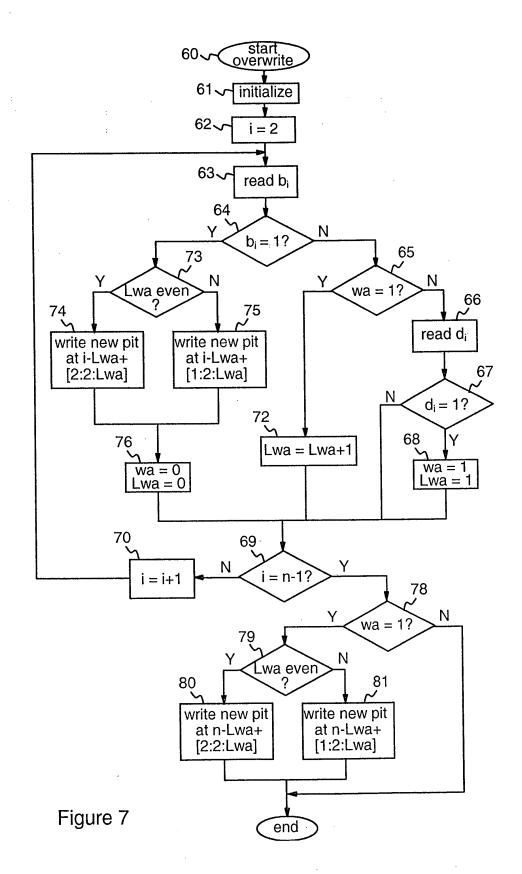
					· · · · · · · · · · · · · · · · · · ·	<b>-</b>	<del> </del>	<b>→</b> V		are a =			<del> </del>	; ;			pai bit	ҽ
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
old data d <sub>i</sub> :	1	0	1	0	0	1	0	0	0	1	0	1	0	0	1	0		
coded data b <sub>i</sub> :	0	0	0	0	1	0	1	0	0	1	0	0	1	0	0	0		
reads:	R	_	-	-	-	-	-	-	R	-	-	R	-	-	R	-		
writes:	-	W	W	W	W	-	W	· <u>-</u>	-	W	-	-	W	-	-	W	W	

Figure 4

						4		<b>-</b> ∨		e are						S	spare bit	Э
		◀														-	-	
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
old data d <sub>i</sub> :	1	0	1	0	0	1	0	0	0	1	0	1	0	0	1	0		
coded data b <sub>i</sub> :	0	0	0	0	1	0	1	0	0	1	0	0	1	0	0	0		
reads:	R	-	-	-	-	-	-	-	-	-	-	-	-	-	R	-		
writes:	-	W	W	W	W	-	W	-	-	W	-	-	W	-	-	W	W	

Figure 6



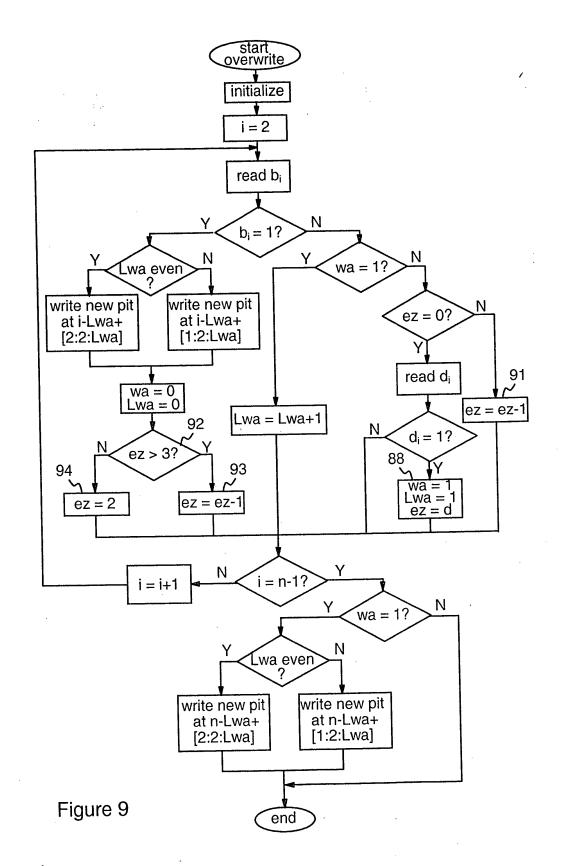


							-		te a va =		S					s	pare bit
	;	-				<b></b>			: ;	<b>→ →</b>			:			ł	<b>↓</b>
bit positions i:	0	1_	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
old data d <sub>i</sub> :	1	0	0	1	0	0	0	0	1	0	0	0.	1	0	0	1	
coded data b <sub>i</sub> :	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	
reads:	R	-	-	-	-	-	R	R	R	-	R	R	-	R	R	R	
writes:	-	w	-	W	-	W	-	-	-	w	-	-	W	-	-	-	W
write results		•					•	•		•	•						
i = 1:	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0	1	
i = 3:	0	0	0	1	0	0	0	0	1	0	0	0	1	0	0	1	
i = 5:	0	0	0	0	0	1	0	0	1	0	0	0	1	0	0	1	
i = 9:	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	1	
i = 12:	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	1	
i = 16:	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	1

Figure 8

						<b>-</b>			te a va =	rea = 1)	S					S	pare bit
		-															•
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
old data d <sub>i</sub> :	1	0	0	1	0	0	0	0	1	0	0	0	1	0	0	1	
coded data b <sub>i</sub> :	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	
reads:	R	-	-	-	-	-	-	-	-	-	-	-	-		-	R	
writes:	-	W	-	W	-	W	-	-	-	W	-	-	W	-	-	-	W

Figure 10



Code:	(1, 3)	(2, 10) code 1	(2, 10) code 2
N <sub>w</sub>	0.41	0.38	0.37
N <sub>r</sub>	0.22	0.42	0.41
N <sub>method2</sub>	0.44	0.61	0.57
N <sub>code</sub>	0.37	0.2	0.22

Figure 11

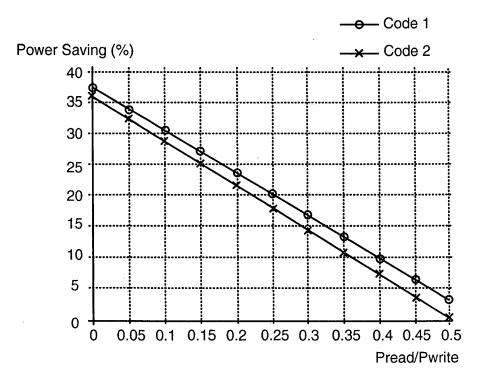


Figure 12

Code:	(2, 10) code 1	(2, 10) code 2
N <sub>w2</sub>	0.24	0.25
N <sub>r2</sub>	0.29	0.26
N <sub>method3</sub>	0.3	0.29
N <sub>code</sub>	0.2	0.22

Figure 13

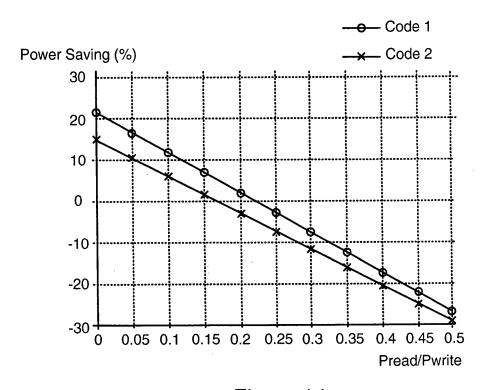


Figure 14